

Notice of Allowability

Application No.

09/450,491

Examiner

Abbas I Abdulsalam

Applicant(s)

YOKOYAMA, RYOICHI

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 08/12/04.
2. ☒ The allowed claim(s) is/are 1-18.
3. ☒ The drawings filed on 29 November 1999 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

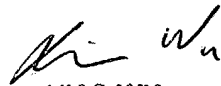
* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____


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PRIMARY EXAMINER

DETAILED ACTION

Allowable Subject Matter

1. The following is an examiner's statement of reasons for allowance:

Yamazaki et al. (USPN 6689492) teach an EL display device, with an EL element 203, which comprises a pixel electrode (anode) 46, and EL layer 47 and a cathode 48, to make a structure in which heat generated by the EL element 203 is radiated. Yamazaki et al. teach a base film 12 that is especially effective for cases in which a substrate containing mobile ions, or a substrate having conductivity is used.

Regarding claim 1, none of the cited references teaches or suggests an active-type light emitting display comprising: a light emission panel including light emission elements each having a first electrode, a second electrode, and an emissive portion, and thin film transistors for respectively driving the light emission elements; the light emission elements and at least one of the thin film transistors are formed on or above a same substrate; the first electrode is electrically connected to at least one of the thin film transistors and is formed over an insulating layer, which is formed covering the at least one of the thin film transistors; the second electrode is formed in a pattern that is common for a plurality of pixels, above the first electrode; the thin film transistor includes a first conductive material that is formed between the substrate and the first electrode, the first conductive material is formed below the second electrode; a connection conductor for connecting the second electrode and a signal supply portion, the signal supply portion supplying a signal to the second electrode for controlling the second electrode separately from the first electrode; and the connection conductor having a section between the second electrode and the

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signal supply portion, at least a part of the section being a multiplayer structure formed of a second electrode material used for the second electrode and a second conductive material used for transistors, the first conductive material is the same material as the second conductive material, the multiplayer structure having a resistance lower than a resistance of a signal layer of the second electrode material.

Regarding claim 9, none of the cited prior art teaches or suggests an active-type electro luminescent display comprising: a light emission panel including light emission elements each having a first electrode, a second electrode, and a luminous portion, and thin film transistors for respectively driving the light emission elements; the light emission elements and at least one of the thin film transistor are formed on or above a same substrate; the first electrode is electrically connected to at least one of the thin film transistors and is formed over an insulating layer, which is formed covering the at least one of the thin film transistors; the second electrode is formed in a pattern that is common for a plurality of pixels, above the first electrode; the thin film transistor includes a first conductive material that is formed between the substrates and the first electrode, the first conductive material is formed below the second electrode; a connection conductor for connecting the second electrode and a signal supply portion, the signal supply portion supplying a signal to the second electrode for controlling the second electrode separately from the first electrode; and the connection conductor having section between the second electrode and the signal supply portion, at least a part of the section being formed of a second conductive material for the first conductive material is the same material as the second conductive material, the part

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having resistance lower than a resistance of a single layer of a material used for the second electrode.

Regarding claim 17, none of the cited prior art teaches or suggests an active-type light emitting display comprising: a light emission panel including light emission elements each having a first electrode, a second electrode, an emissive portion, and thin film transistors for respectively driving the light emission elements; the light emission elements and at least one of the thin film transistor are formed on or above a same substrate; the first electrode is electrically connected to at least one of the thin film transistors and is formed over an insulating layer, which is formed covering the at least one of the thin film transistors; the second electrode is formed in a pattern that is common for a plurality of pixels, above the first electrode; and the thin film transistor includes a first conductive material that is formed between the substrate and the first electrode, the first conductive material is formed below the second electrode; a connection conductor for connecting the second electrode and a signal supply portion, the signal supply portion supplying a signal to the second electrode for controlling the second electrode separately from the first electrode; the connection conductor having a section between the second electrode and the signal supply portion, at least a part of the section being a multiplayer structure formed of a second electrode material used for the second electrode and a second conductive material for the first conductive material is the same material as the second conductive material, the multiplayer structure having a resistance lower than a resistance of a single layer of the second electrode material.

Regarding claim 18, none of the cited prior art teaches or suggests an active-type electro luminescent display comprising: a light emission panel including light emission elements each having a first electrode, a second electrode, and a luminous portion, and thin film transistors for respectively driving said emission elements; said light emission elements and at least one of said thin film transistors are formed on or above a same substrate; said first electrode is electrically connected to at least one of said thin film transistors and is formed over an insulating layer, which is formed covering said at least one of said thin film transistors; said second electrode is formed in a pattern that is common for a plurality of pixels, above said first electrode; said thin film transistor includes a first conductive material that is formed between said substrate and said first electrode, and said first conductive material is formed below said second electrode; a connection conductor for connecting said second electrode and a signal supply portion, said signal supply portion supplying a signal to said second electrode for controlling said second electrode separately from said first electrode; and said connection conductor having a section between said second electrode and said signal supply portion, at least a part of said section being formed of a second conductive material used for said thin film transistors, said first conductive material is the same material as said second conductive material, said part having a resistance lower than a resistance of a single layer of a material used for said second electrode.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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2. Any inquiry concerning this communication or earlier communication from the examiner should be directed to **Abbas Abdulsalam** whose telephone number is **(703) 305-8591**. The examiner can normally be reached on Monday through Friday (9:00-5:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **Richard Hjerpe**, can be reached at **(703) 305-4709**.

Any response to this action should be mailed to:

Commissioner of patents and Trademarks

Washington, D.C. 20231

or faxed to:

(703) 872-9314

Hand delivered responses should be brought to Crystal Park II, Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology center 2600 customer Service office whose telephone number is (703) 306-0377.

Abbas Abdulsalam

Examiner

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September 14, 2004


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